Claims

1 (Previously Amended) A method for updating microcode, comprising the steps of:

assigning a first LUN to a first device;

assigning a second LUN to a memory;

wherein said first LUN and said second LUN are separate;

said first device receiving one or more commands;

said first device obtaining a LUN address from each of said one or more commands; and

in response to said LUN address obtained from each of said one or more commands being equal to said second LUN, updating said microcode in said memory using said LUN address assigned to said second LUN by processing each of said one or more commands.

2 (Original) The method of claim 1, further comprising:

in response to said LUN address obtained from each of said one or more commands being equal to said first LUN, processing each of said one or more commands as input/output commands of said first device.

3 (Original) The method of claim 1, further comprising:

in response to said first device receiving a prepare for microcode update command, placing said first device is a operational state to receive said update of said microcode.

4 (Original) The method of claim 3, wherein said placing said first device is a operational state to receive said update of said microcode further comprises:

not accepting any new commands for processing;

completing all current commands; and

placing movable components at a rest position.

5 (Original) The method of claim 1, wherein said processing each of said one or more commands to update said microcode further comprises:

overwriting a memory associated with said first device with an updated microcode.

6 (Previously Amended) A system for updating microcode, comprising:

a first device addressable by a first LUN; and

a memory addressable by a second LUN, wherein said first LUN and said second LUN are separate, and wherein said first device receives one or more commands, obtains a LUN address from each of said one or more commands and in response to said LUN address obtained from each of said one or more commands being equal to said second LUN, updating said microcode in said memory using said LUN address assigned to said second LUN by processing each of said one or more commands.

7 (Original) The system of claim 6, further comprising:

a host, wherein said host sends microcode update commands to said first device.

8 (Original) The system of claim 6, further comprising:

a host; and

a device interface coupled to said host, wherein said device interface receives commands from said host and transfers said commands to LUN addressable components.

9 (Original) The system of claim 6, wherein said memory is an Electrically Erasable Programmable Read Only Memory.

10 (Original) The system of claim 6, wherein said memory is coupled to said first device.

11 (Original) The system of claim 6, further comprising

an accessor, wherein said memory is coupled to said accessor.

12 (Original) The system of claim 6, further comprising:

a second device removably attached to said first device, wherein said memory is coupled to said second device.

13 (Original) The system of claim 6, further comprising:

a controller for operating said first device, wherein said memory is coupled to said controller.

14 (Original) The system of claim 6, wherein said system is an automated data storage library.

15 (Currently Amended) An article of manufacture comprising a ~~data storage~~ computer readable medium tangibly embodying a program of machine-readable instructions

executable by a digital processing apparatus to perform method steps for updating microcode of a first device assigned to a first LUN, said first device coupled to a memory assigned to a second LUN, wherein said first LUN and said second LUN are separate, said method comprising the steps of:

said first device receiving one or more commands;

said first device obtaining a LUN address from each of said one or more commands; and

in response to said LUN address obtained from each of said one or more commands being equal to said second LUN, updating said microcode in said memory using said LUN address assigned to said second LUN by processing each of said one or more command.

16 (Original) The article of manufacture of claim 15, wherein said method further comprises:

in response to said LUN address obtained from each of said one or more commands being equal to said first LUN, processing each of said one or more commands as input/output commands of said first device.

17 (Original) The article of manufacture of claim 15, wherein said method further comprises:

in response to said first device receiving a prepare for microcode update command, placing said first device is a operational state to receive said update of said microcode.

18 (Original) The article of manufacture of claim 17, wherein said wherein said placing said first device is a operational state to receive said update of said microcode further comprises:

not accepting any new commands for processing;

completing all current commands; and

placing movable components at a rest position.